

# Can J-Link interfaces be used with ST-Link



## Overview

Preferably the J-Link debug plug-in would be used, but, for costs reasons, ST did not provide the standard JTAG connector, so it is currently not possible to connect to these boards with an external J-Link probe. SEGGER offers a firmware upgrading the ST-LINK on-board on the Nucleo and Discovery Boards. This firmware makes the ST-LINK on-board compatible with J-Link OB, allowing users to take advantage of most J-Link features like the ultra fast flash download and debugging speed or the free-to-use. ST Link is a programming and debugging tool developed by STMicroelectronics specifically for their microcontrollers, including the STM32 series. For a few limited tests, the OpenOCD plug-in can be. First, simply explain the relationship between them: JTAG is a parallel agreement; Link is a converter that allows JTAG to support serial port USB; ulink is a KEIL-specific emulator, add some functions; ST-LINK is a special simulation of STM8 and STM32. The interface protocol of many ARM chips is JTAG.

## Article Content

### ST-LINK On-Board

This firmware makes the ST-LINK on-board compatible with J-Link OB, allowing users to take advantage of most J-Link features like the ultra fast flash download and debugging speed or the free-to-use ...

### Debugging STM32 with OpenOCD and a Segger J-Link on Linux

If you have to work with the STM32 (or any other device for that matter) and want to use an external debugger like the J-Link be sure to find a development board equipped with the ARM ...

### Using J-Link with the STM32 boards

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### Understanding the Differences: ST Link vs. JLink

Choosing between ST Link and J-Link depends on your specific development needs, budget, and software compatibility requirements. Both tools have their pros and cons, offering robust solutions in ...

### What is JTAG, JLINK, ULINK, ST-LINK? the difference?

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### Enabling SEGGER RTT Logging by Converting ST-Link to J-Link for ...

In this article, our aim is to address these difficulties and provide guidance on transitioning from ST-Link to J-Link for establishing connections between STM32 and other devices.

### Do you know the difference between JLink, ST-Link, ULink ...

J-Link is a JTAG emulator launched by German SEGGER company to support the emulation of ARM core chips. The interface protocol of many ARM chips is JTAG. One end of JLink ...

### ST-LINK/V2 | Tool

The ST-LINK/V2 is an in-circuit debugger and programmer for the STM8 and STM32 microcontrollers. The single-wire interface module (SWIM) and JTAG/serial wire debugging (SWD) interfaces are used ...

### Would you recommend a Segger J-Link over ST-Link for a junior?

The Segger J-Link-EDU use to be the best ARM debugger for hobbyists at \$60, but because of chip shortages during the COVID era, they discontinued it except for special orders.

Using J-Link with the STM32 boards

Overview  
Convert The St-Link On-Board Into A J-Link  
The Custom J-Link to St Cable  
The St 6-Pin SWD Connector  
The J-Link 20-Pin JTAG/SWD Connector  
The Target Detect Strap  
Jumpers  
Conclusion  
The ST boards have a pair of jumpers, that need to be installed, for the programming signals to be connected from the 6-pin connector to the target processor. See more on [gnu-mcu-eclipse.github.io](https://gnu-mcu-eclipse.github.io)  
Programmer Sought

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JTAG & SWD Pinout Guide: ARM 10-pin, 20-pin, ST ...

Find JTAG and SWD pinouts for common debug headers and probes. Compare ARM 10-pin, 20-pin, ST-Link, J-Link, Tag-Connect, and more.

## Contact Us

For more information, pricing, or custom solutions, please contact us:

Website: <https://budowasilesia.pl>

Email: [contact@budowasilesia.pl](mailto:contact@budowasilesia.pl)

Phone: +48 537 192 846

Address: ul. Chorzowska 45, 40-001 Katowice, Silesian Voivodeship, Poland

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